

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: James P. Baukus, et al.)	Re: Formal Drawings
Serial No.: Not yet assigned)	
Filed: concurrently herewith)	
Divisional of USSN 09/882,900)	
Filed on June 15, 2001)	Our Ref: B-3947DIV 621039-4
For: "PROGRAMMABLE CONNECTOR/ ISOLATOR AND DOUBLE POLYSILICON LAYER CMOS PROCESS WITH BURIED CONTACT USING THE SAME")	Date: July 14, 2003

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

ATTN: OFFICIAL DRAFTSMAN

Sir:

Enclosed herewith are four (4) sheets of drawings (1-7), which should be entered into the above-identified application to replace the drawings presently on file.

Respectfully submitted,



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Enclosures: four (4) sheets of drawings (1-7)